

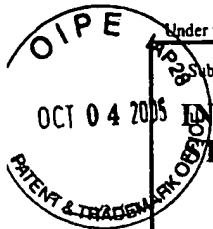
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	09/583,087
				Filing Date	February 1, 2000
				First Named Inventor	Shunpei YAMAZAKI
				Group Art Unit	2815
				Examiner Name	J. Jackson, Jr.
Sheet	1	of	1	Attorney Docket Number	0756-2203

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	02-067817			03/07/1990		Abst.

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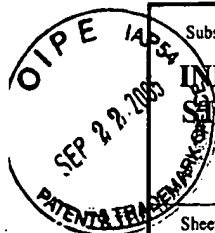
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	58-088787			05/26/1983	Full
		JP	54-154992			12/16/1979	Full
		JP	64-010711			01/13/1989	Abst.
		JP	01-236731			09/21/1989	Abst.
		JP	02-067819			03/07/1990	Abst.

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		H. YANAI, 3.3.2 Basic Circuit of Analog Switch, Semiconductor Handbook, 2 <sup>nd</sup> Edition, Pages 842-843, 1977.	Concise Statement

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